



Arm Architecture Reference Manual

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An authoritative, insider's guide to the world's most widely-used 32-bit RISC microprocessor architecture.

- Presents both an architecture overview and a programmer's model.
- Introduces the ARM instruction set and code sequences.
- ARM RISC chips power 90% of the world's mobile phones -- and are expected to be at the heart of many low-cost network computer solutions.

This is the only complete guide to ARM, the world's most widely-available 32-bit RISC microprocessor architecture.

Produced by the architects that are actively working on the ARM specification, this book contains detailed information about all versions of the ARM and Thumb™ instruction sets, the memory management and cache functions, and optimized code examples. Both an architectural overview and programmer's model are presented. Coverage also includes 26-bit architectures and the System Control Coprocessor.

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